

## Function tables (NJU6676 vs. S1D15605)

2004.6.25

		NJU6676CL	S1D15605D00B
Supply Voltage	[V]	2.2---5.5	1.8---5.5
LCD Driving Voltage	[V]	VDD-18	VDD-18
Display Size		65com/132seg	65com/132seg
DD RAM Size		65x132bits	65x132bits
Duty		1/65	1/65
LCD Bias		1/7,1/9	1/7,1/9
Voltage Booster		2---4 times	2---4 times
Reference Voltage		VDD-VSS (0%/°C)	-2.1V(-0.05%/°C) -4.9V(-0.2%/°C)
EVR		64steps	64steps
External Feedback Resistors (Ra,Rb)		Only External	Internal or External
Terminals		Without IRS/HPM terminals	IRS/HPM terminals
External Clock Operation	Input Terminal	OSC1 terminal	CL terminal
	CLS Terminal	CLS="H"	CLS="L"
Interface		8-Parallel Serial	8-Parallel Serial
Substrate		N	N
PAD Pitch	[um]	60	72
Package		Bump Chip	Bump Chip

# Instruction Table (NJU6676 vs. S1D15605)

\*Added / removed instructions are shown in yellow.

**NJU6676**

**S1D15605**

2004.6.25

Instruction	Code										Code	Note																
	A0	RD	WR	D7	D6	D5	D4	D3	D2	D1			D0															
a Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0	1	0	1	0	1	0	1	0	1	1	1	0	1				
b Display Start Line Set	0	1	0	0	1	Address						0	1	Address														
c Page Address Set	0	1	0	1	0	1	1	Address					0	1	0	1	1	Address										
d Column Address Set	0	1	0	0	0	0	1	Upper Address				0	1	0	0	0	0	1	Upper Address									
	0	1	0	0	0	0	0	Lower Address				0	1	0	0	0	0	0	Lower Address									
e Status Read	0	0	1	Status				0	0	0	0	0	0	1	Status				0	0	0	0						
f Display Data Write	1	1	0	Write Data								1	1	0	Write Data													
g Display Data Read	1	0	1	Read Data								1	0	1	Read Data													
h ADC Selct	0	1	0	1	0	1	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	0	1					
i Inverse Display ON/OFF	0	1	0	1	0	1	0	0	1	1	0	1	0	1	1	0	1	1	0	1	0	1	1					
j Entire Display ON/OFF	0	1	0	1	0	1	0	0	1	0	0	1	0	0	1	0	0	1	0	0	1	0	1					
k LCD Bias Select	0	1	0	1	0	1	0	0	0	1	0	1	0	1	0	0	0	1	0	1	0	1	1					
l Read Modify Write	0	1	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
m End	0	1	0	1	1	1	0	1	1	1	0	1	1	1	0	1	1	1	0	1	0	1	0					
n Reset	0	1	0	1	1	1	0	0	0	1	0	0	0	1	0	0	0	1	0	0	1	0	0					
o Common Direction Select	0	1	0	1	1	0	0	0	*	*	*	1	1	0	0	0	*	*	*	1	0	0	*					
p Power Control Set	0	1	0	0	0	1	0	1	Power Control				0	1	0	0	0	1	0	1	Power Control							
q Driver ON/OFF	0	1	0	1	1	1	0	0	1	1	0	1	0	1	1	0	1	1	0	1	0	1	1	Only NJU6676.				
r V5 Voltage Adjust Internal Resistor Ratio Set	-	-	-	-	-	-	-	-	-	-	-	0	1	0	0	0	1	0	0	Resistor Ratio			Only S1D15605.					
s EVR Mode Set	0	1	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	1					
EVR Register Set	0	1	0	*	*	Register						0	1	0	*	*	Register											
t Static Indicator ON/OFF	0	1	0	1	0	1	0	1	1	0	0	1	0	1	1	0	1	0	0	1	0	0	1					
Static Indicator Register Set	0	1	0	*	*	*	*	*	*	*	Register				0	1	0	*	*	*	*	*	*	Register				
u Power Save	0	1	0	1	0	1	0	1	1	1	0	0	1	0	1	1	1	1	0	0	1	0	1					
	0	1	0	1	0	1	0	0	1	0	1	0	1	0	0	1	0	1	0	1	0	1	1					
v NOP	0	1	0	1	1	1	0	0	0	1	1	0	1	1	1	0	0	0	1	1	0	1	1					
w Test	0	1	0	1	1	1	1	*	*	*	*	0	1	1	1	1	*	*	*	*	*	*	*					

\*:Don't Care  
-:No Function