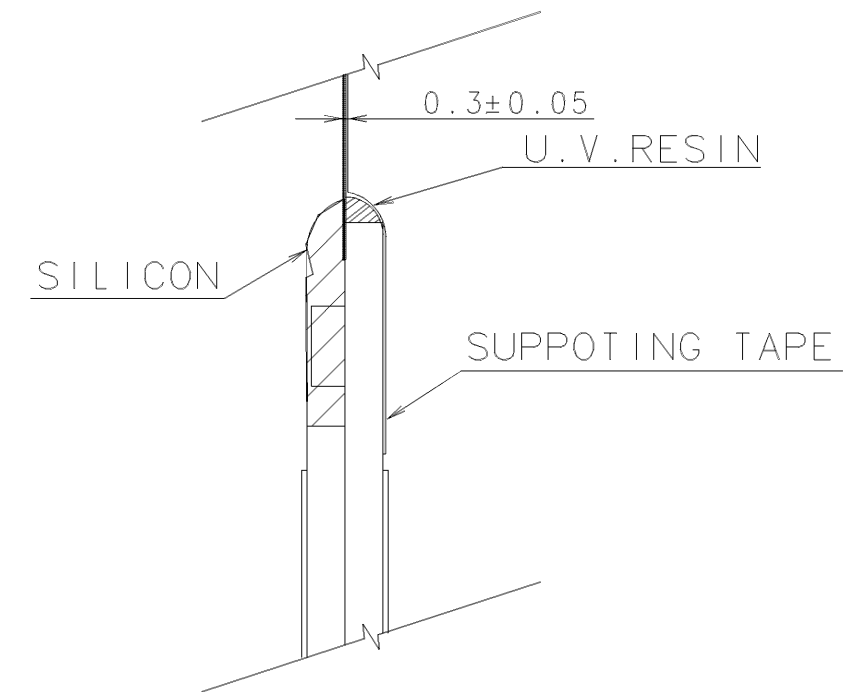
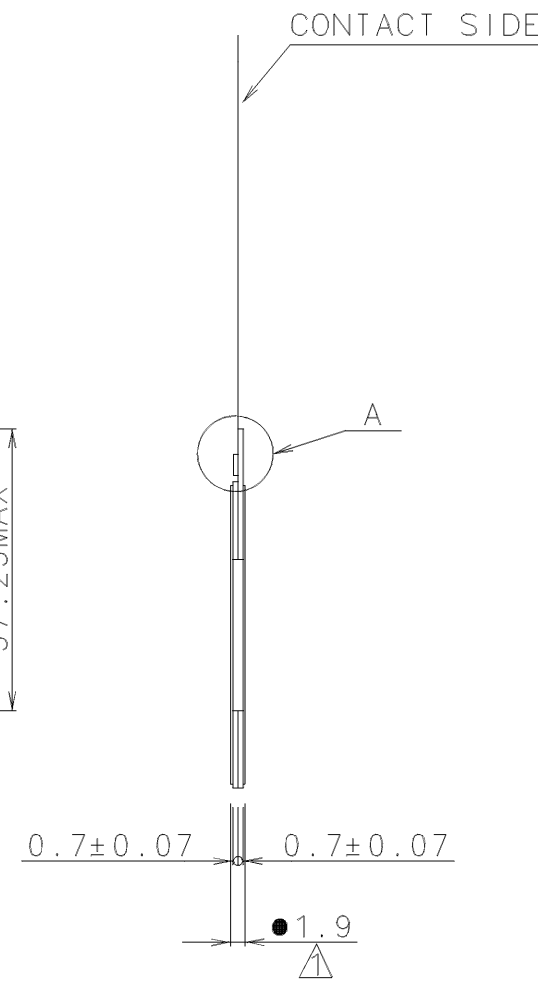


CN1 PIN ASSINGMENT

NO.	SYMBOL	NO.	SYMBOL	NO.	SYMBOL
1	NC	11	D4	21	CAP2-
2	/CS1	12	D5	22	CAP2+
3	/RES	13	D6	23	V1
4	A0	14	D7	24	V2
5	/WR	15	VDD	25	V3
6	/RD	16	GND	26	V4
7	D0	17	VOUT	27	V5
8	D1	18	CAP3-	28	VR
9	D2	19	CAP1+	29	C86
10	D3	20	CAP1-	30	IRS



NOTES

- THE MARK OF SHOWS THE MOLD AREA OF SILICON RESIN(MAX.)
 - *THE SILICON SHOULD NOT PROTRUDE FROM THE EDGE OF LCDP AND SHOULD NOT EXCEED THE HEIGHT OF FRONT POLARIZER.
 - *MUST COVER ALL ITO TRACES

TOLERANCE			THIRD ANGLE PROJECTION	DATE JUL, 10, 01	OPTREX CORPORATION
MEASURE	A	B			
L ≤ 16	±0.1	±0.3	±1	UNIT mm	APPROVED S.KOBAYASHI
16 < L ≤ 63	±0.2	±0.5	±1.5	SCALE 1:1	TITLE DIMENSIONAL OUTLINE
63 < L ≤ 250	±0.3	±0.8	±2		CHECKED M.KINOSHITA
250 < L ≤ 500	±0.5	±1.2	±3	DRAWN T.KATO	DWG NO UE-310802A
500 < L	±0.8	±2	±4		
ANGLE	±1°	±5°	±10°		